

## Atom-Thick Materials for the Next Revolution in Electronics

Han Wang, Lili Yu, Allen Hsu, Xu Zhao, Benjamin Mailly,  
Charles Mackin, Yi-Hsien, Yumeng Shi, Mildred Dresselhaus, Jing Kong, and **Tomás Palacios**

Massachusetts Institute of Technology  
77 Massachusetts Ave., Bldg. 39-567B, Cambridge, MA-02139, USA  
[tpalacios@mit.edu](mailto:tpalacios@mit.edu)

### Abstract

Electronics is at a crossroads. The materials and technologies that have enabled the information revolution of the last 60 years are quickly reaching their ultimate physical limit. Fortunately, a new generation of atom-thick materials has recently been discovered. This talk will review these new materials, all of them less than one nanometer thick, and the novel devices and applications enabled by their amazing properties.

Graphene was the first one of these materials to be discovered. A two-dimensional structure of carbon atoms with  $sp^2$  bonding, graphene has demonstrated the highest electron and hole mobility at room temperature in any semiconductor material. Its transport properties make it ideal for all kind of non-linear analog electronics, it shows extremely high frequency performance, and its one-atom-thickness enables transparent and highly flexible electronics.

MoS<sub>2</sub> and other transition metal dichalcogenides (TMD) materials are another example of two-dimensional materials with unique properties. Made of only three-atoms thick, they can complement graphene to build flexible digital and mixed-signal circuits, overcoming its lack of bandgap while still sharing many of graphene's excellent mechanical and thermal properties. We will describe some of our recent results on the development of 2D nanoelectronics on MoS<sub>2</sub> and TMD materials. First, large-area single-layer MoS<sub>2</sub> material is grown by chemical vapor deposition (CVD) that makes the wafer-scale fabrication of MoS<sub>2</sub> devices and circuits possible for the first time. Second, the top-gated transistors, fabricated for the first time on single-layer MoS<sub>2</sub> grown by CVD, show multiple state-of-the-art characteristics, such as high mobility, ultra-high on/off current ratio, record current density and current saturation. Finally, the first fully integrated digital and analog circuits based on MoS<sub>2</sub> are constructed to demonstrate its capability for both logic and mixed-signal applications. Key circuit building blocks for digital and analog electronics such as inverter, NAND gate, memory and ring oscillator circuits are demonstrated.

### References

- [1] H. Wang, A. L. Hsu, and T. Palacios, "Graphene electronics for RF applications," *Microwave Magazine, IEEE*, vol. 13, no. 4, pp. 114–125, 2012.
- [2] H. Wang, L. Yu, Y.-H. Lee, Y. Shi, A. Hsu, M. L. Chin, L.-J. Li, M. Dubey, J. Kong, and T. Palacios, "Integrated Circuits Based on Bilayer MoS<sub>2</sub> Transistors," *Nano Lett.*, vol. 12, no. 9, pp. 4674–4680, Sep. 2012.

### Figures

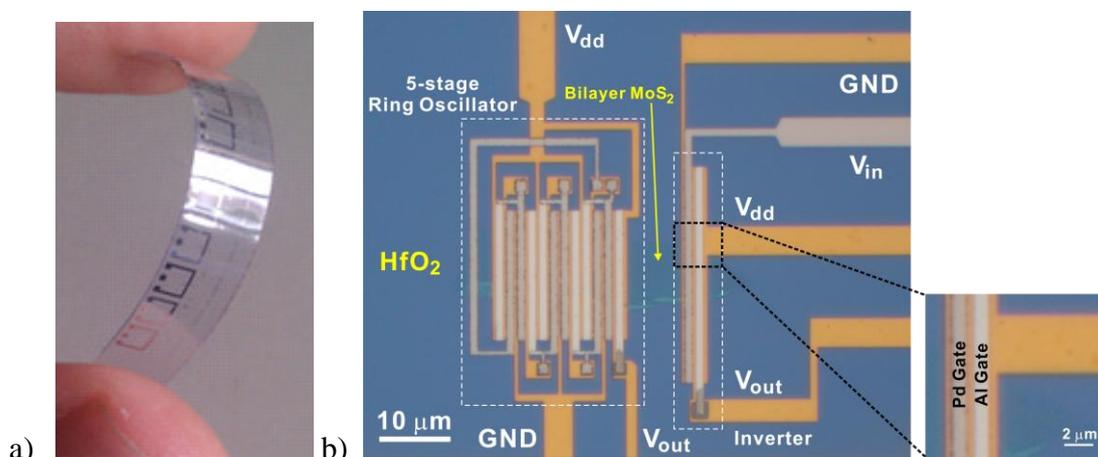


Figure 1. a) Graphene transistors and chemical sensors transferred to a transparent, flexible substrate. b) Optical micrograph of a MoS<sub>2</sub> integrated circuit, a 5-stage ring oscillator.