

## **TRANSISTOR FROM INDIVIDUAL NANOTUBES**

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The state of the art of preparing transistors based on individual single-walled carbon nanotubes will be reviewed. The "standard" nanotube transistor consists of a nanotube adsorbed on a silicon chip, contacted by e-beam lithographic metal leads, and switched by a large-area backgate. This transistor will be contrasted to the "all-carbon" transistor, where the gate is also a carbon nanotube and to the "peapod transistor", where the nanotube is "doped from the inside" by filling with metallofullerenes. The importance of in-situ visualizing the nanotubes in a transmission electron microscope will be pointed out and a technique will be presented which allows for such a visualisation.