

## ROOM-TEMPERATURE FUNCTIONAL NANO-ELECTRONIC DEVICES AND CIRCUITS WITH BALLISTIC JUNCTIONS

*Hongqi Xu*

*Division of Solid State Physics/The Nanometer Structure Consortium, Lund University, Box 118,  
S-221 00 Lund, Sweden*

Email: Hongqi.Xu@ftf.lth.se; Phone: +46 46 222 7759; Fax: + 46 46 222 3637

Present digital logic gates are primarily built from field effect transistors (FETs) such as complementary metal oxide semiconductors with the gate length in the deep sub- $\mu\text{m}$  regime. As device feature sizes approach the nanometer regime, fundamental physical constraints and increasingly prohibitive economic costs will make further miniaturization in electronic circuits difficult, and this has motivated efforts worldwide to search for new strategies to meet expected computing demands of the future. In the strategy for device fabrication, bottom-up approaches to electronic devices with well-defined nanoscale building blocks have been demonstrated. An alternative strategy is to develop new device concepts by making a revolutionary departure from the FET-based paradigm. The approach must exploit the emerging inherent properties of nanostructures. Here we report on a paradigm based on multi-terminal ballistic junctions (MBJs), in which the ballistic nature of electron transport, which has emerged in the nanostructures, is exploited.

A MBJ is a nanostructure-compatible device formed from three or more ballistically coupled nonlinear conductors, such as quantum point contacts realizable by standard nanofabrication methods. A key requirement in the realization of this device is that carrier transport in the device is ballistic or quasi-ballistic. Various MBJ structures have been fabricated on high-quality semiconductor heterostructures and novel properties of MBJs have been found [1-6]. In particular, we demonstrate that MBJs can be used as diodes and triodes, as well as a building block for logic circuits. These devices function at room temperature, and the device layout features fabrication by a single-step lithography process that is suitable for mass production with low-cost approaches such as nanoimprint lithography. Furthermore, the demonstrated device principles are expected to apply equally well to multi-terminal junctions made from, e.g., molecules and nanotubes.

### References:

- [1] H.Q. Xu, *Electrical properties of three-terminal ballistic junctions*, Appl. Phys. Lett. 78, 2064 (2001).
- [2] I. Shorubalko et al., *Nonlinear operation of GaInAs/InP-based three-terminal ballistic junctions*, Appl. Phys. Lett. 79, 1384 (2001).
- [3] I. Shorubalko et al., *A novel frequency-multiplication device based on three-terminal ballistic junction*, IEEE Electron Device Lett. 23, 377 (2002).
- [4] H.Q. Xu, *Diode and transistor behaviors of three-terminal ballistic junctions*, Appl. Phys. Lett. 80, 853 (2002).
- [5] I. Shorubalko et al., *Tunable nonlinear current-voltage characteristics of three-terminal ballistic nanojunctions*, Appl. Phys. Lett. 83, 2369 (2003).
- [6] H.Q. Xu et al., *Novel nanoelectronic triodes and logic devices with TBJs*, IEEE Electron Device Lett. 25, 164 (2004).