## Air stable n-type black phosphorus transistor with photoactive doping layer

## Yih-Ren Chang<sup>†</sup>

Po-Hsun Ho,<sup>†</sup> Min-Ken Li,<sup>†</sup> Raman Sankar,<sup>‡</sup> Fu-Yu Shih,<sup>§</sup>,<sup> $\perp$ </sup> Shao-Sian Li,<sup>†</sup> Wei-Hua Wang,<sup> $\perp$ </sup> Fang-Cheng Chou,<sup>‡</sup>,<sup> $\parallel$ </sup> Chun-Wei Chen<sup>\*,†, $\parallel$ </sup>

*†* Department of Materials Science and Engineering, *‡* Center for Condensed Matter Sciences, and § Department of Physics, National Taiwan University, No. 1, Sec. 4, Roosevelt Rd., Taipei 10617, Taiwan

∠Institute of Atomic and Molecular Sciences, Academia Sinica, Taipei, 10617, Taiwan

// Taiwan Consortium of Emergent Crystalline Materials (TCECM), Ministry of Science and Technology, 106, Sec. 2, Heping E. Rd., Taipei 10622, Taiwan

## r04527027@ntu.edu.tw

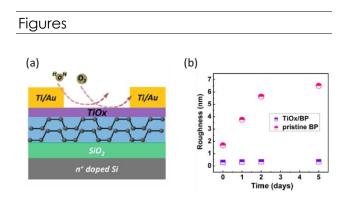
## Abstract

Black phosphorus (BP) is usually a p-type semiconductor with hole mobility of its device range from 100cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup> to 300 cm<sup>2</sup>V<sup>-</sup> <sup>1</sup>s<sup>-1</sup> and on/off ratio up to 10<sup>5</sup>. However, BP is very sensitive to the environment and its outstanding properties will fade away in a short time under moisture and oxygen exposure.

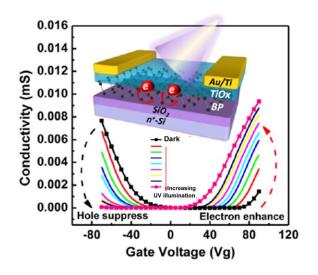
In this presentation, a novel approach to demonstrate tunable transport properties of a few-layered BP field-effect transistor (FET) device with extended air stability will be proposed. With Titanium suboxide (TiOx) thin film applied on BP surface we can not only improve its air stability but also provide tunable n-type doping on BP through light illumination which can modify the transport properties of BP from the intrinsic holeto the dominated transport electrondominated transport. With controllable transport properties on channel, this novel device structure of BP exhibit great potential for the future development of logic devices or optoelectronic devices.[1]

References

[1] P.H. Ho et al., ACS photonics, Vol.3 issue6 (2016) pp1102–1108



**Figure 1:** (a) Schematic representation of a BP FET with precoated TiOx layer. (b) Timedependent evolution of surface roughness of BP with and without coating TiOx film.



**Figure 2:** Evolution of conductivity versus gatevoltage curves for the TiOx/BP FET with increased irradiation time. The inset is the schematic representation of a BP/TiOx transistor under illumination