

Synthesis of Graphene on dielectric substrates using a modified filtered vacuum arc system

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Abstract

Here we present a reliable process to deposit graphene directly on silicon oxide, quartz, and mica (muscovite) using a solid carbon source. The process uses a pulsed filtered vacuum arc system to deposit a small but well defined amount of carbon homogeneously on heated substrates. The substrates are tilted with respect to the particle beam and a noble gas provides an inert atmosphere. To prevent the buildup of agglomerations during the coating process a filtered arc system is used. Filtered arc evaporators produce a nearly droplet-free plasma stream of ionized atoms with particle energies of some tens of eV. By this way, carbon atoms will slightly be implanted and fixed at their place of arrival. Consequently, they cannot move over the surface and there cannot grow islands as it normally accrues with atoms or molecules of thermal energies.

In the present system, a pulsed filtered vacuum arc system is used to deposit a precisely defined amount of carbon atoms, needed to produce a full covered graphene layer. The Raman spectrum is dominated by the characteristic peaks "D", "G", and "2D" which are the typical signatures of sp^2 hybridized graphitic carbon (Figure 1, blue line).

The 2D peak (Figure 2) is split into four components: $2D_{1B}$, $2D_{1A}$, $2D_{2A}$, $2D_{2B}$. This is characteristic for multilayer graphene between two and five layers [1]. The position of the 2D-peak at 2700 cm^{-1} is shifted to a higher wavenumber as expected for few-layer graphene on silicon oxide [1,2].

The homogeneity of the graphene is shown by measuring the intensity of the 2D band over an area of $(100\text{ }\mu\text{m} \times 100\text{ }\mu\text{m})$. The resulting false color picture shows neither holes nor any partial inhomogeneity (Figure 3, insert).

The best carbon layers have a surface resistance of $3 \cdot 10^3\text{ }\Omega_{\square}$, measured by means of a 4-wire van-der-Pauw arrangement. The specific resistivity is found to be $1.5\text{ }\mu\Omega\text{cm}$, and is independent on the number of layers

The filtered vacuum arc technology provides a completely metal free process for the graphene fabrication on insulating substrates. It can be used as a stable, large area graphene production, which is compatible to established CMOS and other semiconductor fabrication.

References

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Figures

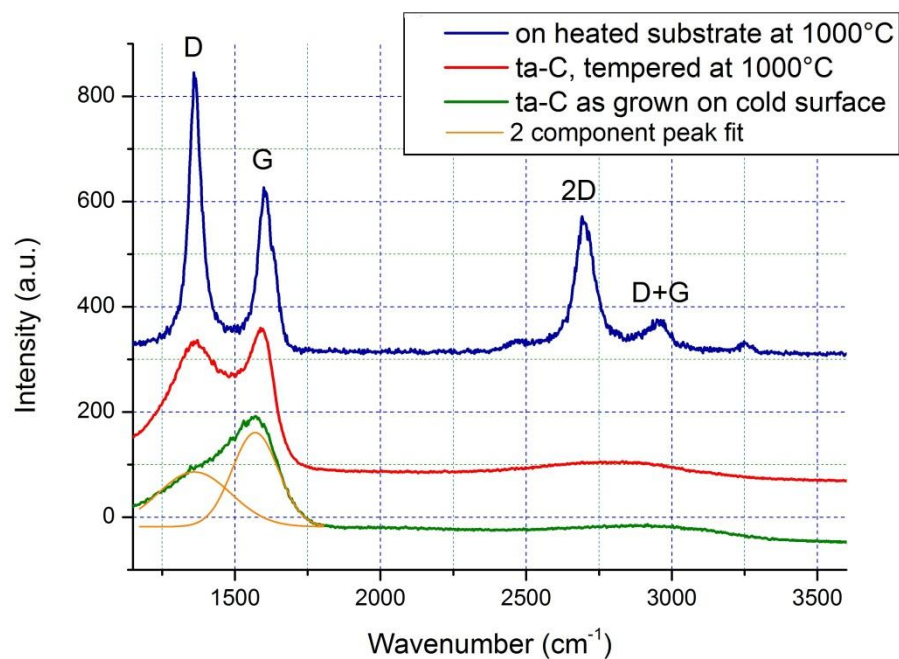


Figure 1: Comparison of Raman spectra of different carbon layers. Conventional ta-C layer (green) split into its 2 components “D” and “G” (orange line); Annealed ta-C layer on quartz (red line); graphene, deposited on heated quartz (blue line).

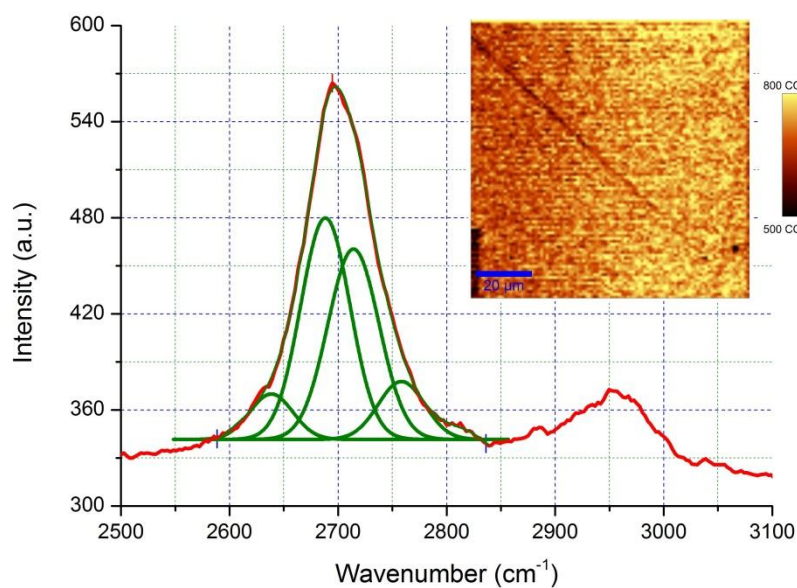


Figure2: 2D-Peak of graphene split in four components, insert: Large area scan of 2D-intensity (100 μm x 100 μm)